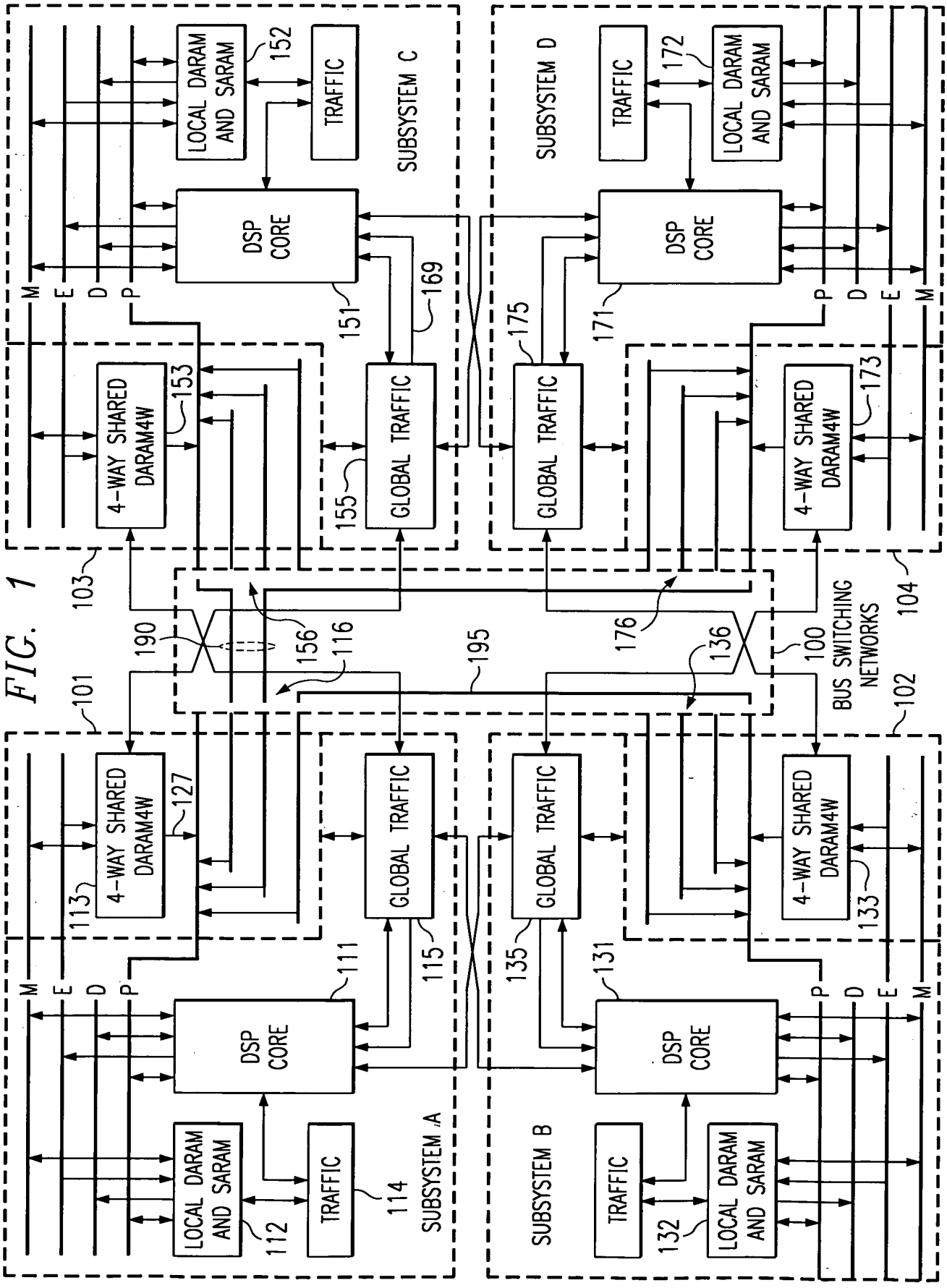
05T30 82420T

FIG. 1



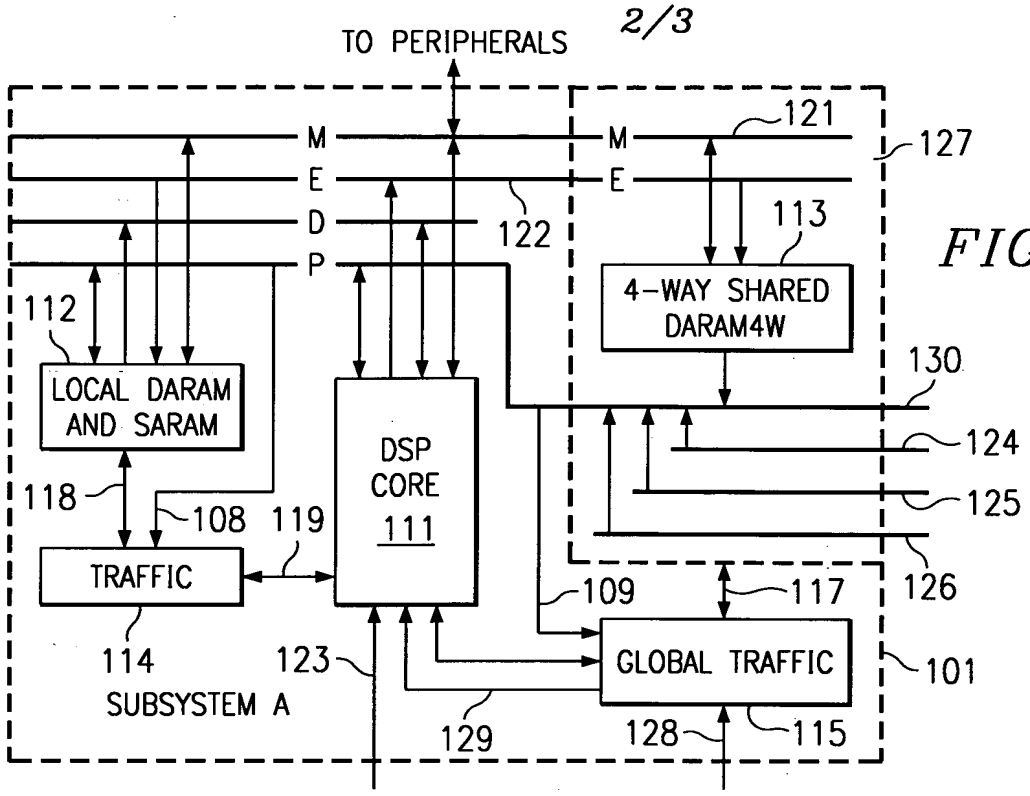

TO PERIPHERALS
M
E
D
P
121
127
122
113
4-WAY SHARED DARAM4W
112
LOCAL DARAM AND SARAM
130
DSP CORE 111
124
118
108
119
125
TRAFFIC
126
109
117
114
123
GLOBAL TRAFFIC
101
SUBSYSTEM A
129
128
115

FIG. 2

FIG. 3

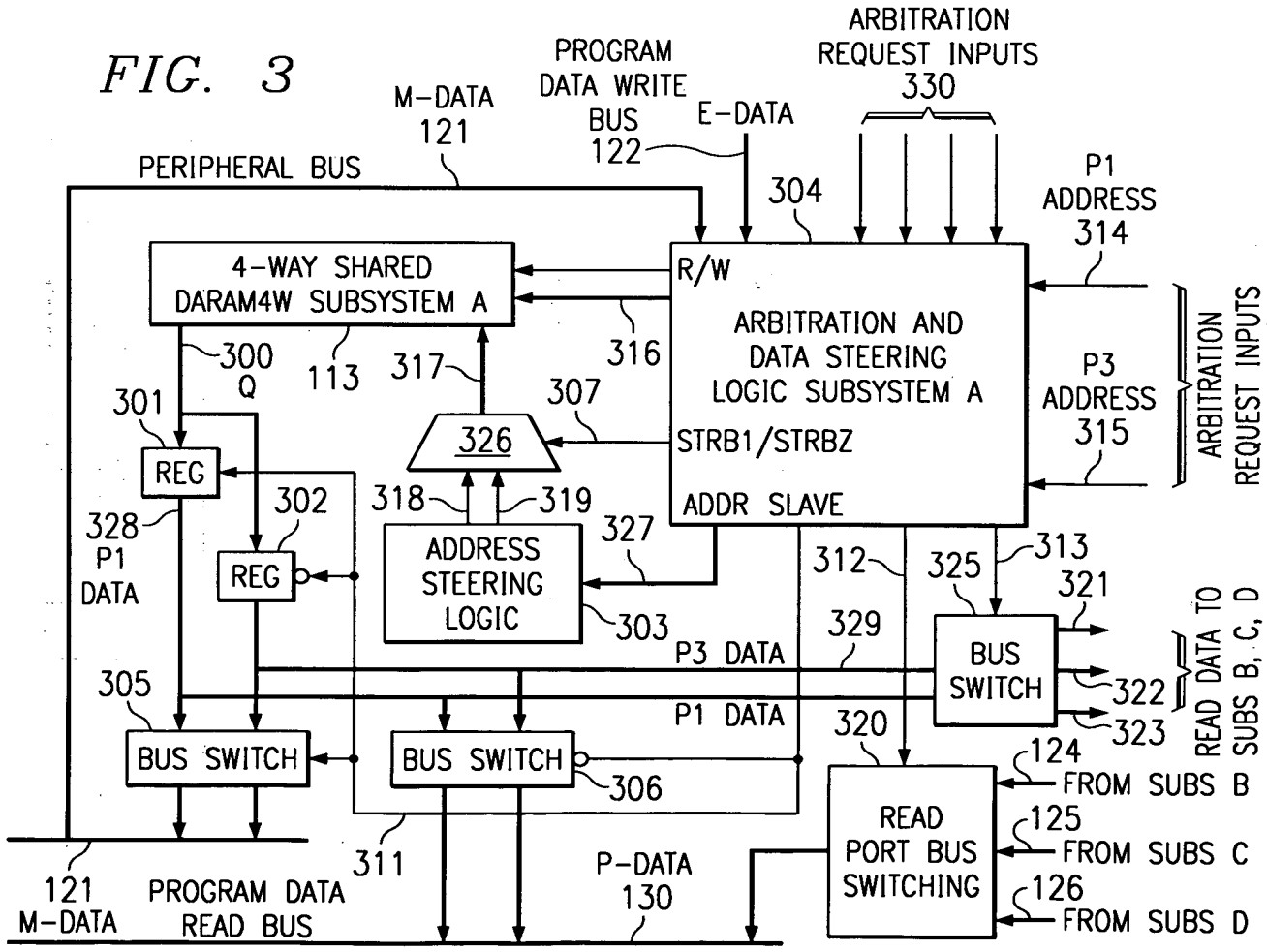

ARBITRATION REQUEST INPUTS
330
PROGRAM DATA WRITE BUS
122
M-DATA
121
E-DATA
PERIPHERAL BUS
304
P1 ADDRESS
314
4-WAY SHARED DARAM4W SUBSYSTEM A
R/W
ARBITRATION AND DATA STEERING LOGIC SUBSYSTEM A
316
300
Q
113
317
307
P3 ADDRESS
315
ARBITRATION REQUEST INPUTS
301
326
STRB1/STRBZ
REG
302
318
319
ADDR SLAVE
328
P1 DATA
REG
ADDRESS STEERING LOGIC
327
312
325
313
321
303
329
P3 DATA
BUS SWITCH
322
READ DATA TO SUBS B, C, D
305
P1 DATA
323
320
BUS SWITCH
BUS SWITCH
124
FROM SUBS B
306
READ PORT BUS SWITCHING
125
FROM SUBS C
311
126
FROM SUBS D
121
M-DATA
PROGRAM DATA READ BUS
P-DATA
130

FIG. 4

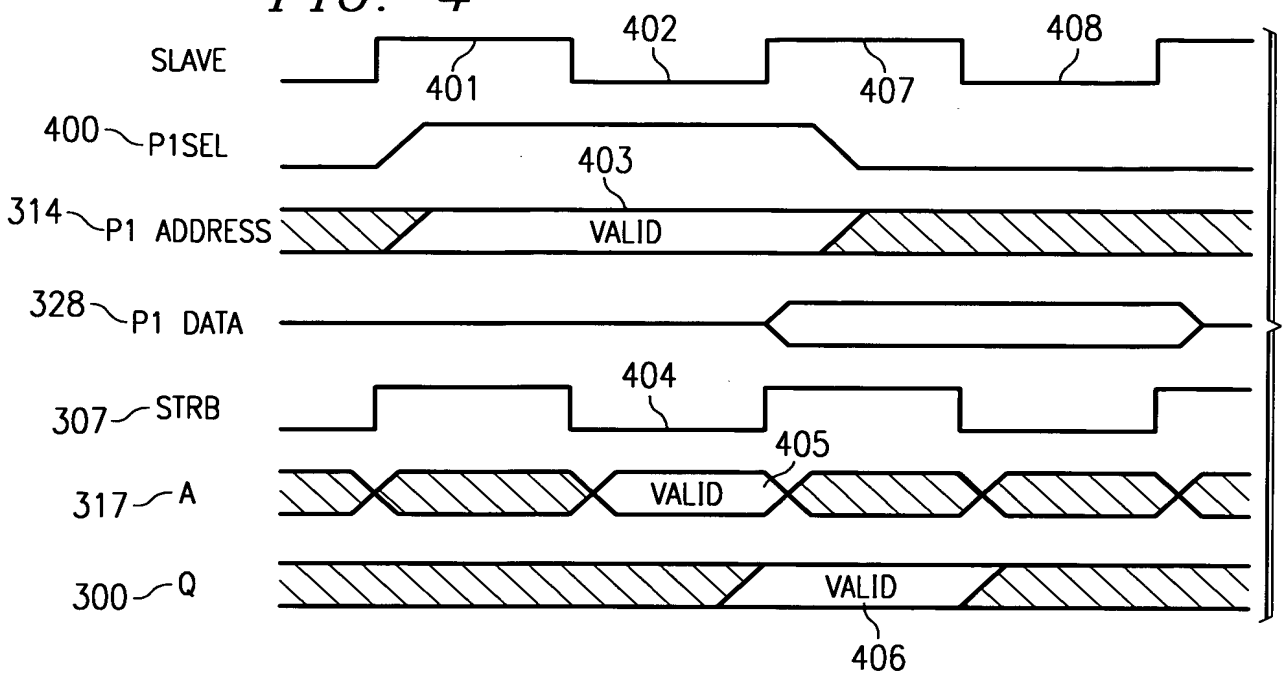
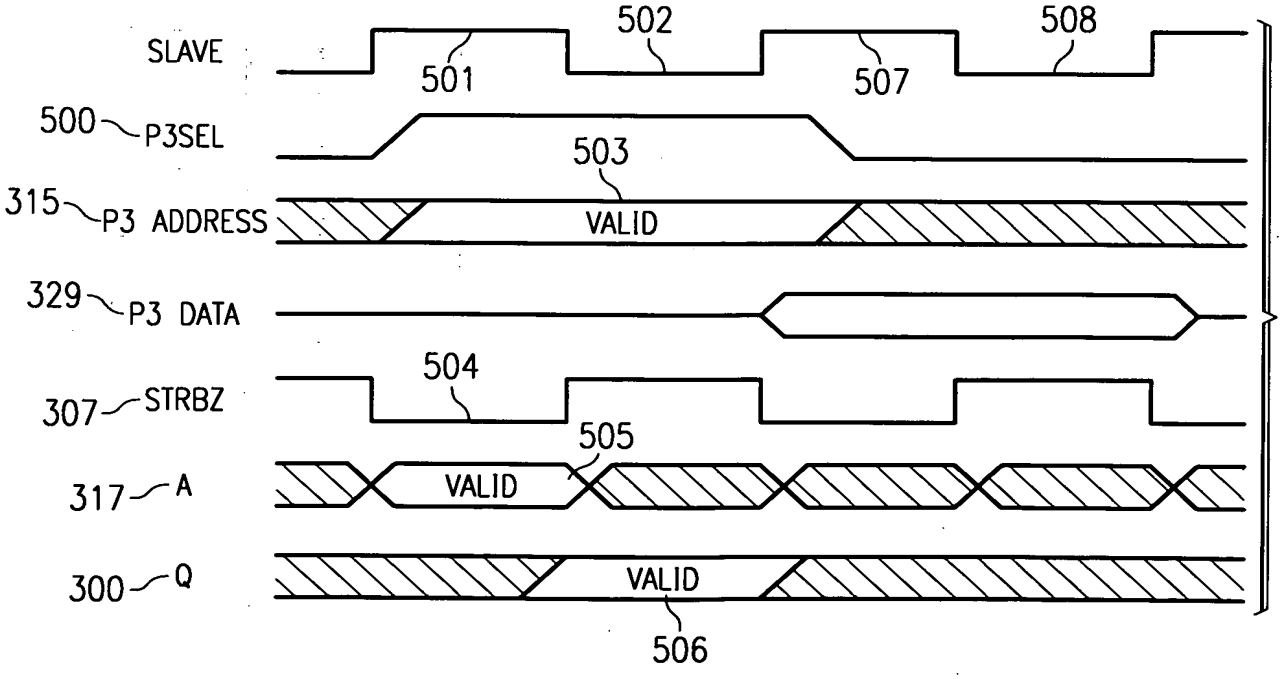


FIG. 5



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